



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Ritsuko Iwasaki

Serial No.: 09/496,421

Group Art Unit: 2815

Filed: February 2, 2000

Examiner: Lee, E.

For: SEMICONDUCTOR DEVICE HAVING AN IMPROVED LAYOUT PATTERN OF
PAIR TRANSISTORS

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated December 19, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims ~~3-5, 11-18, 20-22, and 24-25~~ without prejudice or disclaimer.

Please add the following new claims:

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- 1 - 32. A semiconductor device, comprising:
- 2 a pair of first and second transistors;
- 3 first to third contacts arranged in that order in a first direction;
- 4 a first gate electrode of said first transistor connected to said first contact and
- 5 extending in a second direction;
- 6 a second gate of said second transistor connected to said third contact and extending
- 7 in said second direction; and
- 8 a dummy gate electrode extending in said second direction so as to be sandwiched
- 9 between said first and second gate electrodes,
- 10 said first and second gate electrodes and said dummy gate electrode having a same